



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

BIBDATASHEET**CONFIRMATION NO. 4653**

Bib Data Sheet

SERIAL NUMBER 10/628,601	FILING DATE 07/28/2003 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 31692-73091
-----------------------------	---------------------------------------	--------------	------------------------	---------------------------------------

APPLICANTS

Michael J. Berman, Portland, OR;
George E. Bailey, Welches, OR;
Rennie G. Barber, Gresham, OR;

**** CONTINUING DATA *******
This application is a DIV of 10/138,742 05/03/2002 PAT 6,627,466 *OL*

**** FOREIGN APPLICATIONS *******
none, OL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
**** 01/06/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY OR	SHEETS DRAWING 2	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
--	--	-------------------------------	----------------------------	--------------------------	--------------------------------

ADDRESS
LSI Logic Corporation
1551 McCarthy Blvd.
Milpitas, CA
95035

TITLE
Method and apparatus for detecting backside contamination during fabrication of a semiconductor wafer

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---